

## Applications of Carbon Nanotubes Grown by Chemical Vapor Deposition

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The requirements for using carbon nanotubes as vias and interconnects are described. The growth of high density forests of vertically-aligned carbon nanotubes for interconnect applications by chemical vapor deposition is described. Densities up to  $1.4 \times 10^{13} \text{ cm}^{-2}$  have been achieved by maintaining a small nanotube diameter. The process integration devices is reviewed. © 2012 The Japan Society of Applied Physics

### 1. Introduction

Carbon nanotubes (CNTs) have many potential applications due to their unique electronic, thermal and structural properties.<sup>1-4)</sup> The continued scaling of semiconductor device dimensions in integrated circuits has led to the replacement of many of the traditional materials. Copper has replaced Al as the metal in interconnects, and high dielectric constant oxides such as  $\text{HfO}_2$  have replaced  $\text{SiO}_2$  as the gate insulator in the field effect transistors. Interconnects are the metal wires that connect transistors in the integrated circuit, and are both vertical and horizontally oriented (Fig. 1).

In the near future, the current density carried by interconnects is likely to exceed that which can be carried by copper, due to its electromigration limit of about  $6 \times 10^6 \text{ A/cm}^2$  (Fig. 2).<sup>5)</sup> CNTs or graphene are the only materials that can carry a higher current density, as they are able to carry up to  $10^9 \text{ A/cm}^2$  before failure,<sup>2-4)</sup> due to their strong covalent bonding. A second reason to use CNTs is that their high thermal conductivity might help dissipate heat in the integrated circuit. A third reason is that the large aspect ratio of CNTs might be helpful for making interconnects that have a high aspect ratio, such as through-silicon vias. Finally, CNTs would not require liners to provide diffusion barriers. These advantages have led to considerable effort to develop CNTs for interconnect applications.<sup>6-29)</sup>

Despite these advantages, there have been considerable problems. There are two main difficulties. Firstly, in any economic manufacturing scheme, the CNTs must be grown in place in the via hole or interconnect structure; they cannot be grown elsewhere and placed there. But the standard growth processes are rather incompatible with Si technology. Secondly, interconnects require CNTs of extremely high area density. This arises because CNTs are one dimensional conductors, so that their resistance is the sum of the quantum of conductance per channel  $R_q$  plus a resistance per unit length,  $r$ . The CNT interconnect should have a resistance below that of the copper equivalent to be useful. This requires us to parallel many CNTs to lower the overall resistance,<sup>25)</sup>

$$R = \frac{R_q + r \cdot l}{n},$$

where  $n$  is the number of nanotube conducting channels in parallel. Figure 3 shows that it is necessary to increase  $n$  to of order  $10^{14} \text{ cm}^{-2}$  to be below that of Cu for a typical 100 nm high via. This density equals a pitch of  $\sim 1.1 \text{ nm}$  (on a hexagonal grid). This has recently led to considerable efforts to grow nanotubes in via structures with a maximised area density, guided by the extensive work on the growth of

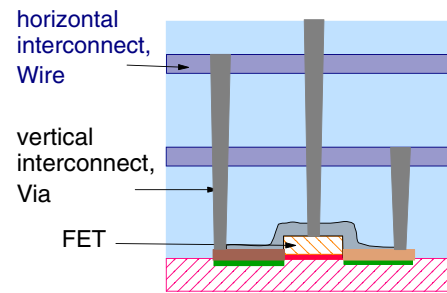


Fig. 1. (Color online) Schematic of horizontal and vertical interconnects in an integrated circuit.

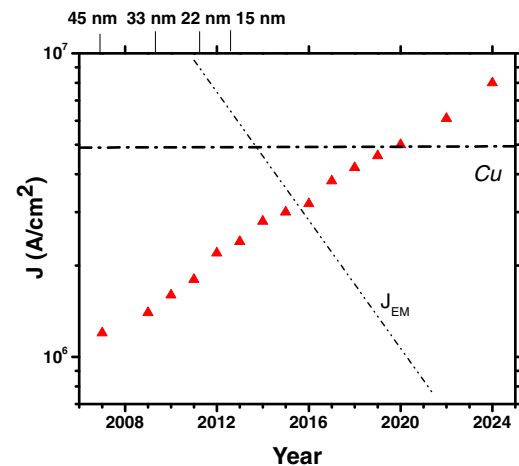


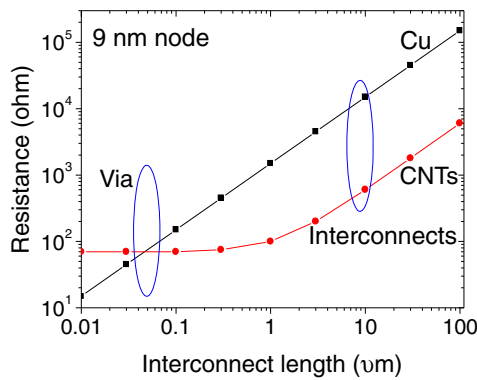
Fig. 2. (Color online) Current density requirements of integrated circuits vs year of development. From Fig. INTC21, ITRS interconnect roadmap.<sup>5)</sup>  $J_{EM}$  is the electromigration limit of Cu for the target lifetime. The horizontal line is the electromigration limit of macroscopic Cu.

nanotube forests,<sup>30-55)</sup> as shown in Fig. 4. Overall, there are four requirements for nanotubes for interconnects, from Robertson *et al.*,<sup>26)</sup>

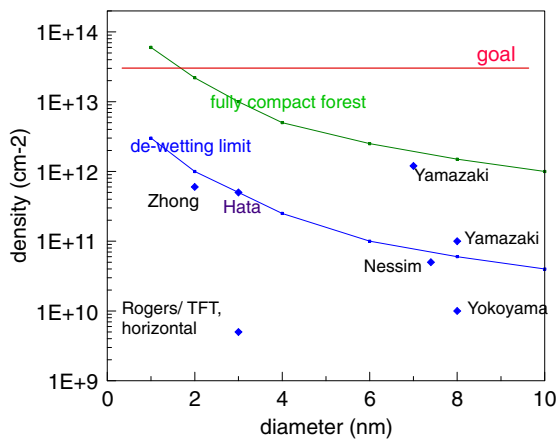
- high density,
- growth on conducting substrates,
- growth at low temperatures ( $400^\circ\text{C}$ ) compatible with a back end of line (BEOL) process,
- a process integration with the rest of the integrated circuit.

Of these, the emphasis has been on density and low temperature.

Figure 4 compares the area density of various grown nanotube samples to the density of a fully dense, aligned



**Fig. 3.** (Color online) Resistance vs interconnect length of copper and carbon nanotube interconnects, for a nanotube density of  $10^{14} \text{ cm}^{-2}$ .

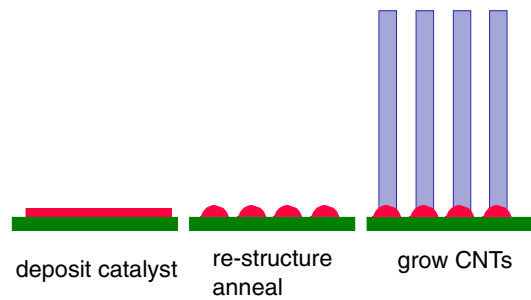


**Fig. 4.** (Color online) Density of some carbon nanotube forests, compared to the theoretical maximum density and a typical density produced by the catalyst de-wetting method.<sup>17,18,20,24,31,38,56</sup>

nanotube forest in which the nanotube cylinders are separated by the *c*-axis spacing of graphite of 0.338 nm. This diagram shows that the tall, vertically-aligned forests grown by Hata *et al.*<sup>30,31</sup> and Zhong *et al.*<sup>38</sup> do not have particularly high densities. Their area densities of  $5 \times 10^{11} \text{ cm}^{-2}$  and  $9 \times 10^{11} \text{ cm}^{-2}$  correspond to only about 5% space filling. On the other hand, work on vias by Nihei *et al.*<sup>12</sup> tended to produce larger diameter multi-wall nanotubes, and their area density was even lower at  $10^{10}$  to  $10^{11} \text{ cm}^{-2}$ . Only recently have such groups achieved densities over  $10^{11} \text{ cm}^{-2}$  and up to  $10^{12} \text{ cm}^{-2}$ .<sup>17–20,29</sup> It is interesting that groups such as Rogers *et al.*<sup>56</sup> growing horizontally-aligned nanotubes for use in thin film transistors achieve densities of about 10 tubes per micrometer, which corresponds to  $10^{10} \text{ cm}^{-2}$ , much lower than required here.

**2. Growth Theory**

CNTs for interconnects are grown by catalytic chemical vapor deposition (CVD). The highest density forests tend to grow by the root growth mechanism. The catalyst is active in its high surface area state of a layer of nanoparticles. The standard method of CVD is a three-step process (Fig. 5); first deposit the catalyst as a thin film by evaporation or sputtering, then transform it into nanoparticles by annealing in a particular atmosphere, and then introduce a hydrocarbon



**Fig. 5.** (Color online) Three step process to produce catalyst, anneal catalyst and grow nanotubes.

**Table I.** Surface energies of solid metals, metallic compounds and oxides (in  $\text{J/m}^2$ ).

	Experiment	Theory	Refs.
Ni	2.08	2.01	60,61
Co	2.22	2.77	60,61
Fe	2.12, 1.72	2.22	60,61
Mo	2.51	3.45	60,61
Cr	2.00	3.5	60,61
W	2.76	4.0	60,61
Ta	2.49	3.08	60,61
Ti	1.75	2.52	60,61
TiN		1.6, 1.40	64
ZrSi <sub>2</sub>	0.8–1.2		59
TiSi <sub>2</sub>	0.8–0.9		59
CoSi <sub>2</sub>	0.72–0.8	1.65	59
NiSi <sub>2</sub>		1.13	59
SiO <sub>2</sub>	0.043–0.106		63
Al <sub>2</sub> O <sub>3</sub>	0.062–0.1	0–0.1	62
MgO	0.1		62

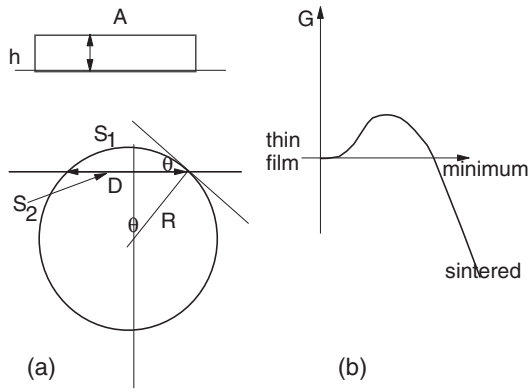
Data of Tyson and Miller<sup>60</sup> are for liquid metals, multiplied by a constant factor of 1.18 to give values for the solid. The values of Vitos *et al.*<sup>61</sup> were calculated by density functional theory, for the lowest energy face of the crystalline phase.

gas to allow growth to occur.<sup>26</sup> It is expected that one nanotube will grow from each catalyst nanoparticle in CVD. In this, CVD growth differs from the high temperature laser-assisted growth in which each catalyst particle tends to nucleate more than one nanotube.<sup>55</sup> Thus, to achieve the highest tube density, we must maximise the density of catalyst nanoparticles.

The transformation of the thin film into nanoparticles is driven by the difference in surface energies of the catalyst metal and the underlying “support” layer.<sup>58</sup> Consider first typical values of the surface energies (Table I).<sup>58–64</sup> The elemental metals have surface energies of typically  $2 \text{ J/m}^2$ , the metallic silicides have surface energies of  $1 \text{ J/m}^2$ , whereas the insulating oxides are much lower at  $0.1 \text{ J/m}^2$ .

The process generally uses this annealing method. It is also possible to use a cluster beam to deposit catalyst nanoparticles.<sup>19</sup> This is produced by condensing gas of metal atoms into nanoparticles using an inert gas stream, particle size separation, and then scanning the beam across the desired area. This process can be employed, however it is much less economic.

The transformation of the thin film catalyst into nanoparticles is driven by the reduction in interfacial energy, see



**Fig. 6.** (a) Geometries of the de-wetting process, and (b) free energies of the de-wetting process.

Figs. 6(a) and 6(b). The surface energy of the catalyst is denoted  $\sigma_1$ , that of the support layer  $\sigma_2$  and the interfacial energy is  $\sigma_{12}$ . The free energy of a thin film of thickness  $h$  of catalyst on a support is given by

$$G_1 = A(\sigma_1 + \sigma_{12}). \quad (1)$$

With reference to the geometries in Fig. 6(a), the free energy of an array of truncated spheroidal particles of diameter  $D$ , area density  $N$  and contact angle  $\theta$  is

$$G_2 = (A - NS_2)\sigma_2 + NS_1\sigma_1 + NS_2\sigma_{12}, \quad (2)$$

where

$$S_1 = 2\pi R^2(1 - \cos \theta), \quad (3)$$

$$S_2 = \pi R^2 \sin^2 \theta. \quad (4)$$

The contact angle is derived from Young's equation,

$$\sigma_2 = \sigma_{12} + \sigma_1 \cos \theta \quad (5)$$

and the conservation of catalyst volume gives

$$hA = NV_{\text{cat}}, \quad (6)$$

where

$$V_{\text{cat}} = \frac{\pi R^3}{3}(1 - \cos \theta)^2(2 + \cos \theta). \quad (7)$$

De-wetting occurs if [see Fig. 6(b)]

$$G_2 \leq G_1, \quad (8)$$

or, by using eqs. (1), (2), and (7)

$$A(\sigma_2 - \sigma_1 - \sigma_{12}) + N(S_1\sigma_1 - S_2\sigma_2 + S_2\sigma_{12}) < 0. \quad (9)$$

Using eq. (5), this gives

$$-A\sigma_1(1 + \cos \theta) + N\sigma_1(S_1 - S_2 \cos \theta) < 0 \quad (10)$$

and  $\sigma_1$  cancels through. Substituting for  $A$  from eq. (6), and using eqs. (3) and (4) gives

$$\frac{R}{3}(1 - \cos \theta)^2(2 + \cos \theta) \geq h[2 - (1 + \cos \theta) \cdot \cos \theta] \quad (11)$$

or

$$R = \frac{3h}{1 - \cos \theta}. \quad (12)$$

Using the particle's projected diameter  $D = 2R \sin \theta$ , this is

$$D = 6h \left( \frac{1 + \cos \theta}{1 - \cos \theta} \right)^{1/2}, \quad (13)$$

and the particle density is found to be

$$N = \frac{(1 - \cos \theta)}{9\pi h^2(2 + \cos \theta)}, \quad (14)$$

or

$$N = \frac{4(1 + \cos \theta)}{\pi D^2(2 + \cos \theta)}.$$

A typical value for  $\theta = 90^\circ$  is  $N = 0.6/D^2$ . Note that this gives the maximum density possible by the de-wetting method.  $G_2$  can lower itself by sintering (increase of the particle diameter) and this lowers  $N$ , as shown in Fig. 6(b).

The key observation is that the maximum  $N$  value obtainable by this method is lower than the maximum theoretical density of an array of single wall CNTs (SWNTs) (Fig. 4).

### 3. Growth of High Density Forests

The best catalysts for growing CNTs are Fe, Co, and Ni. The highest density CNT forests have usually been grown using Fe as catalyst and  $\text{Al}_2\text{O}_3$  as the support layer,<sup>28,36,38,44</sup> for both single-walled and multiwalled forests. One reason for the good performance of Fe on  $\text{Al}_2\text{O}_3$  is that there is an interfacial chemical reaction between the Fe and  $\text{Al}_2\text{O}_3$  which produces an Fe aluminate, as seen by *in-situ* X-ray photoemission spectroscopy (XPS).<sup>50</sup> The interfacial reaction tends to anchor the Fe nanoparticles, and reduce any tendency to sintering. It leads to a narrower distribution of catalyst nanoparticles, of higher density, and thus to a higher density forest. The forest grows by root growth mechanism, and the high density produces the vertical alignment of the forest, by a crowding mechanism.

We have developed two methods to increase the density of the forests. The first method is to reduce the nanotube diameter  $D$ , by reducing the initial thickness of the catalyst film. This increases  $N$ , as  $D$  is proportional to  $h$  from eq. (12) and  $N$  varies as  $h^{-2}$ .

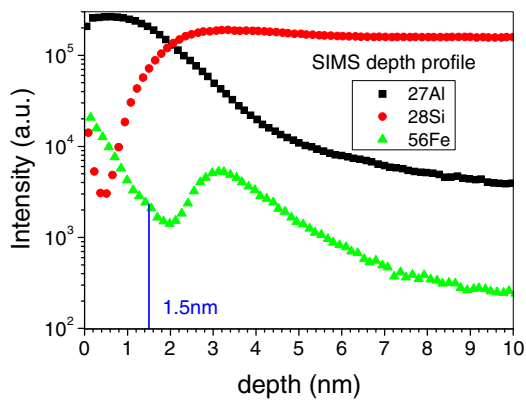
Hasegawa and Noda<sup>55</sup> found that reducing the initial thickness of the Fe film first leads to higher growth, but then causes a loss of yield and limited growth. We analysed the catalyst layer by secondary ion mass spectrometry (SIMS) and Auger. We found that the Fe tends to diffuse into the  $\text{Al}_2\text{O}_3$ , and then build up at the interface between the Si and  $\text{Al}_2\text{O}_3$  where it is blocked (Fig. 7). Thus,  $\text{Al}_2\text{O}_3$  is a useful support layer because its surface properties and often higher surface area leads to lower diffusion on the surface. However, the type of  $\text{Al}_2\text{O}_3$  does affect the forest density as found by Amama.<sup>48</sup> Its porosity can allow the catalyst to diffuse away from the surface, causing a loss of catalyst growth activity. It is necessary to stop this.

We grew the  $\text{Al}_2\text{O}_3$  by depositing a thin layer of Al by evaporation and then carried out a room temperature plasma oxidation of the Al to  $\text{Al}_2\text{O}_3$ . The plasma oxidation creates a densified  $\text{Al}_2\text{O}_3$  with a much reduced Fe diffusion. This allows us to reduce the initial Fe thickness.

The CNTs were grown in a cold wall thermal CVD chamber with a rapid substrate heater, in a hydrogen diluted acetylene gas flow, at a pressure of 15 mbar at 700 °C.

Acetylene is the primary growth species for CNTs, as was shown by molecular beam experiments of Eres<sup>65)</sup> and mass spectrometry studies of Zhong.<sup>43)</sup>

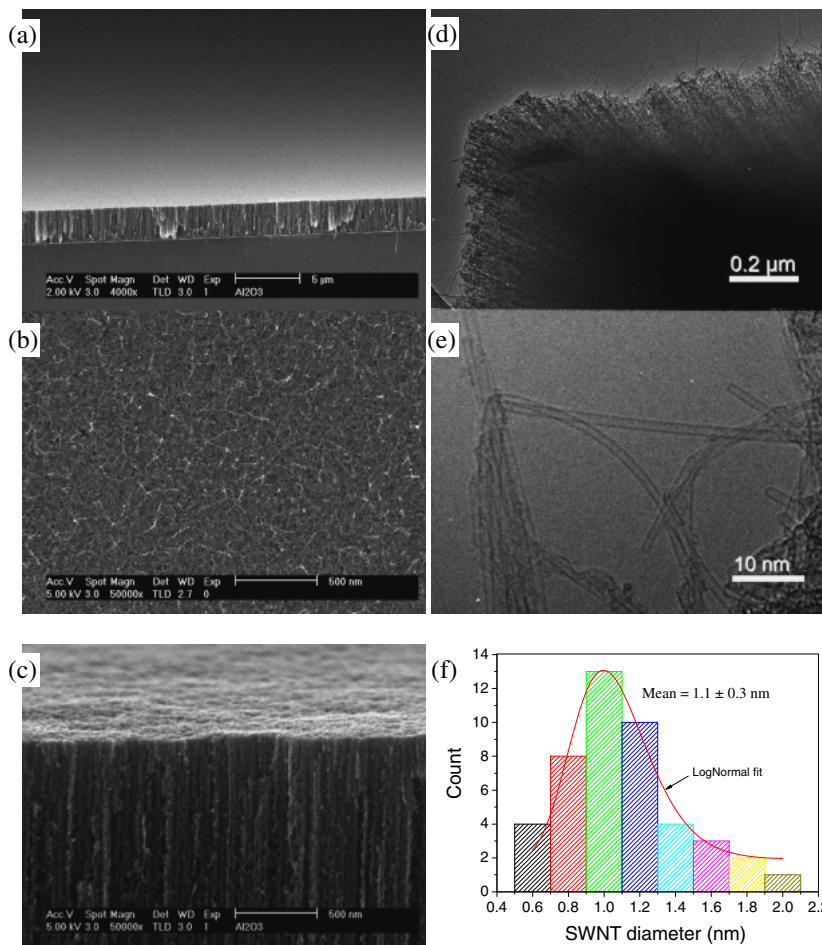
Figure 8 shows high resolution transmission electron microscope (HRTEM; JEOL 4000) and scanning electron microscope (SEM; Philips XL30 SFEG) images of the resulting CNT forest. Due to its higher density, the forest does not grow to millimetres high. The area density is



**Fig. 7.** (Color online) SIMS depth profile of Fe for Fe catalysts on Al<sub>2</sub>O<sub>3</sub> support on Si.

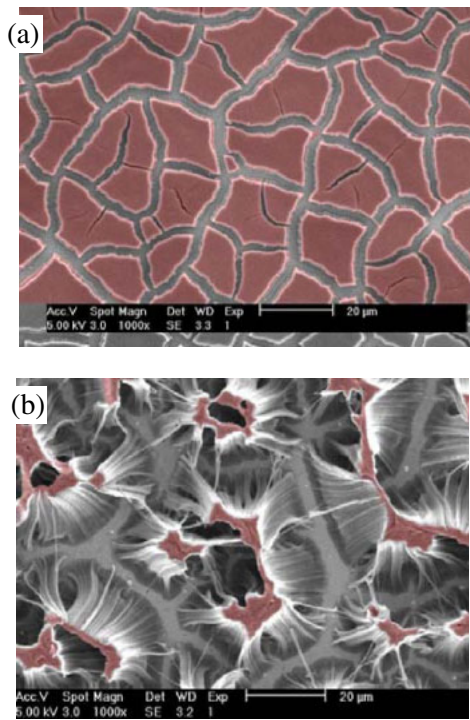
measured in three ways. First, we can count the number of tubes from the SEM images. Second, we can measure the sample's weight gain during growth, measure the diameter and number of walls by TEM, and thereby derive the mass per unit length of the CNT, and from the ratio, this gives the area density. The third way is to use liquid-induced compaction. Applying drops of isopropyl alcohol or ethanol causes the CNT forest to form locally compacted regions. These have previously aroused interest for their patterns by Chakrapani.<sup>66)</sup> Futaba *et al.*<sup>31)</sup> previously estimated the area density from the ratio of the top area of the compacted forest to the total initial area (Fig. 9). The three methods give consistent results. We find that our growth conditions, the average CNT diameter is 1.1 nm, and the tubes are single walled. The mass density of the as-grown layer is 0.245 to 0.345 g/cm<sup>3</sup>, giving an area density of 1.05 to 1.45 × 10<sup>13</sup> cm<sup>-2</sup>. The compacted density is typically 0.47 to 0.7 g/cm<sup>3</sup>, and the ideal compact density of such tubes is 1.53 g/cm<sup>3</sup>. We see that our density of 1.45 × 10<sup>13</sup> cm<sup>-2</sup> is about 30 times higher than the typical density in “super-growth” samples, as shown in Fig. 10.

The second method to grow denser forests is to increase the nucleation density for larger diameter nanotubes by a novel catalyst treatment process, as in Esconjauregui.<sup>67)</sup> The usual annealing process to convert the initial thin film

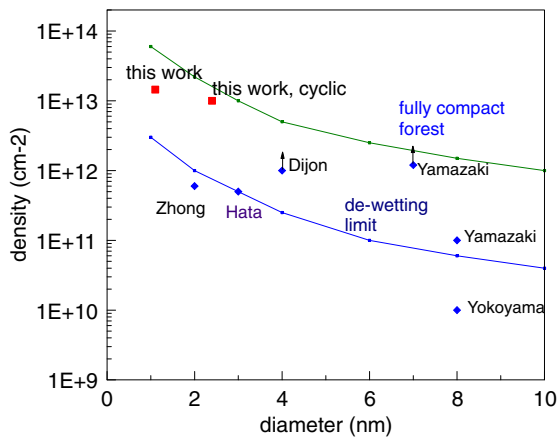


**Fig. 8.** (Color online) SEM (a) and HRTEM (b) images of ultra-high density (UHD) SWNT forests prepared on 0.5 nm Al/0.4 nm Fe/5 nm Al/Si(100) at 15 mbar, 700 °C, 40 sccm C<sub>2</sub>H<sub>2</sub> and 460 sccm H<sub>2</sub> by cold-wall CVD. The 5 nm Al layer had O<sub>2</sub> plasma. (a) A tilt view of the as-grown sample at a low magnification. (b, c) Surface and tilt views at high magnifications. (d, e) Low and high magnification HRTEM images of SWNTs. (f) SWNT diameter distribution from HRTEM images.





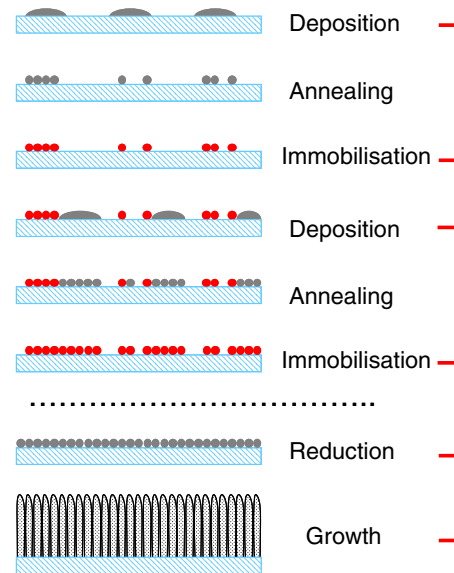
**Fig. 9.** (Color online) Liquid-induced densification of different SWNT forests by soaking the samples with ethanol and drying in air. (a) The ultra high density SWNT sample grown in 1% C<sub>2</sub>H<sub>2</sub>, the inset shows the cross-sectional details of the densification effect. (b) A sample of SWNT forests grown for 1% C<sub>2</sub>H<sub>2</sub> on 0.5 nm Al/0.7 nm Fe/5 nm Al/Si(100), other conditions same as (a).



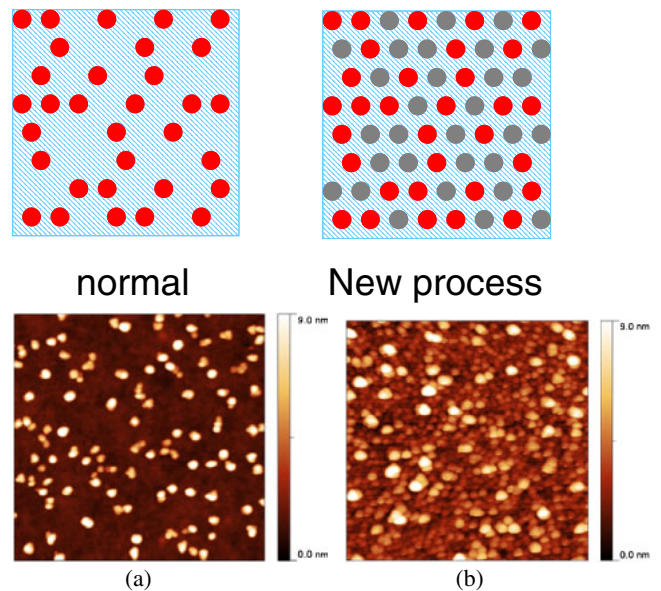
**Fig. 10.** (Color online) Nanotube densities achieved in this work, compared to previous works.<sup>17,18,20,29,31,38</sup> Wall densities are higher than tube densities for MWNTs.

catalyst into nanoparticles follows a fix set of trends from eqs. (12)–(14). This means that if we deposit one layer of catalyst, anneal it, then deposit a second layer and then anneal that, the second layer would merge with the first layer by sintering and give a particle diameter that was twice that from a single layer process, and a particle density  $N$  that was four times lower than from the single layer, rather than a higher density.

There is a way to overcome this limitation by depositing one layer, annealing it, then immobilising the particle layer,



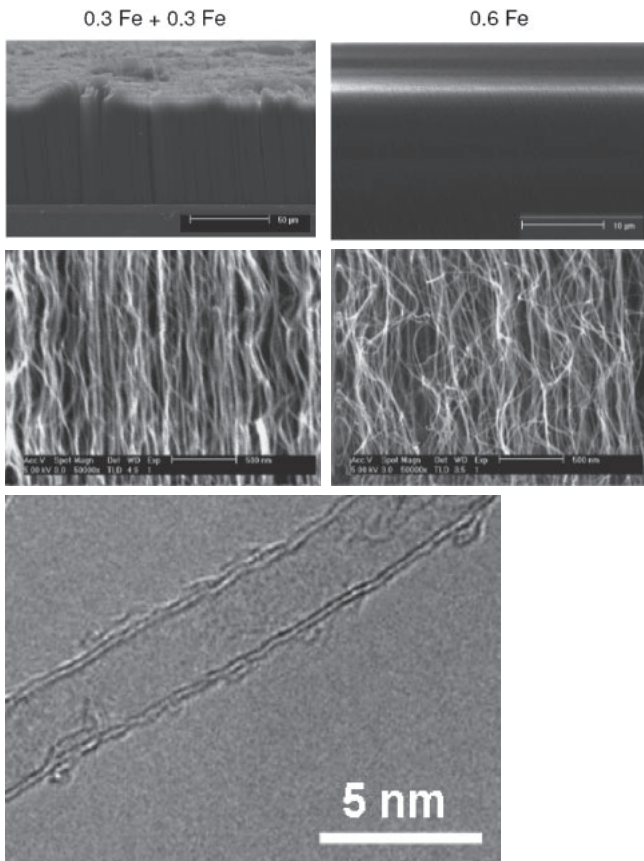
**Fig. 11.** (Color online) Schematic of the cyclic deposition process to produce higher density catalyst nano-particles.



**Fig. 12.** (Color online) schematic of the catalyst nano-particles in (a) the normal catalyst process and (b) the cyclic deposition process, and below AFM images of the catalyst nano-particles.

then depositing another layer and annealing that. This time, the process is sequential, and the density will add cumulatively to a larger number, rather than reduce to a smaller number. Figure 11 shows the method in principle. Figure 12 shows an example of the method in practice, with atomic force microscope (AFM) images of the catalyst particles after one cycle and after two cycles. The experiment was done for a larger than normal film thickness so that the particles were more easily visible by AFM.

The catalyst is prepared using two cycles of 0.3 nm Fe onto Al<sub>2</sub>O<sub>3</sub>. Annealing is carried out in a tube furnace in Ar : H<sub>2</sub> = 1000 : 500 sccm atmosphere at 1 bar, from room temperature up to 750 °C at a heating rate of 75 °C min<sup>-1</sup> for the first cycle and 25 °C min<sup>-1</sup> for the second one. The

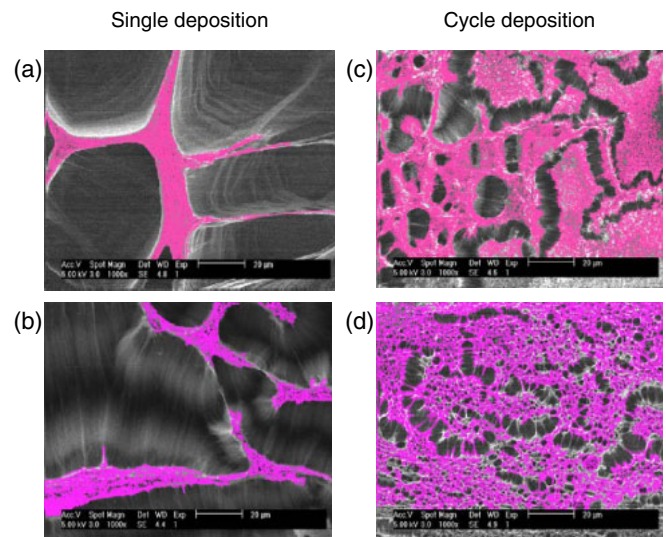


**Fig. 13.** SEM images of the nanotubes produced by (left) the cyclic catalyst process and (right) the normal process, and below a high resolution TEM image of the nanotubes.

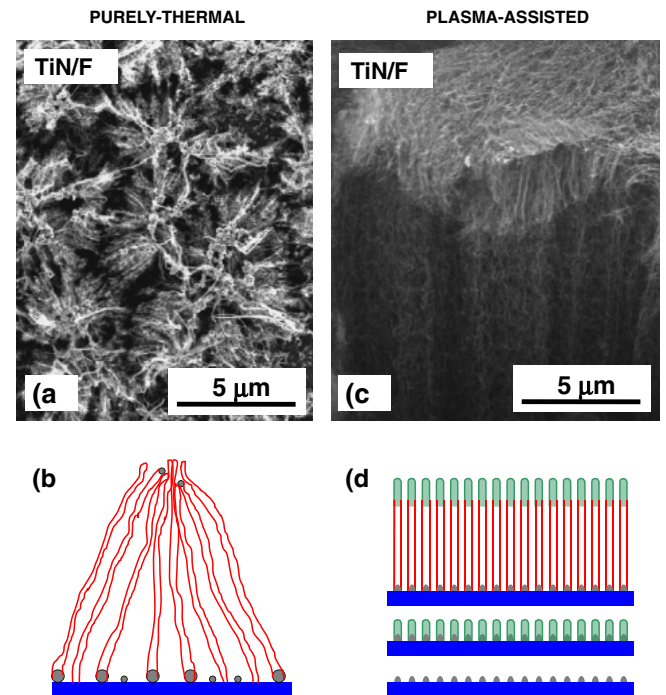
sample is transferred in air between annealing steps. Fe depositions are performed at exactly same conditions. The CNTs were then grown in a furnace tube using Ar : H<sub>2</sub> : C<sub>2</sub>H<sub>2</sub> = 1000 : 500 : 10 sccm for 15 min at 750 °C and 1 bar.

Figure 13(a) shows a high resolution cross sectional field emission gun scanning electron microscope (FEGSEM) image of our ultra-high density nanotube forest, where the tubes are so confined by adjacent tubes that they are much straighter than tubes in forests of previously typical density, such as Fig. 13(c). The high resolution TEM image in Fig. 13(b) shows that the nanotubes are mainly double walled, with mean diameter of 2.4 ± 0.2 nm. Raman finds a G/D peak ratio of over 6. The area density was estimated by the three methods as above, counting, weight gain and liquid induced compaction. Overall, a number of samples were grown with heights of 180–300 nm, CNT diameters of 2.2–2.6 nm, and mass density 0.911 to 1.03 g/cm<sup>3</sup>, with the pitch between nanotube centres of 3.10 to 3.4 nm and resulting area densities of (0.92–1.10) × 10<sup>13</sup> cm<sup>-2</sup>. Figure 14 shows an SEM image of the forest before and after the liquid compaction.

The overall area density results are summarised in Fig. 10. We see that the cyclic density method gives a forest which starts to approach the ideal maximum dense forest. The density could be increase still further by making the diameter smaller. The small density result has a similar density, but is still quite far below the ideal maximum density.



**Fig. 14.** (Color online) SEM image of liquid-induced compacted forest, for (a, b) single catalyst deposition process, and (c, d) cyclic deposition process.

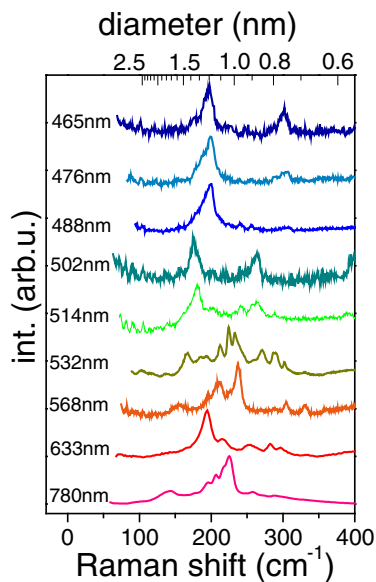


**Fig. 15.** (Color online) Showing how plasma pre-treatment of the catalyst enforces root growth. (a, b) No plasma. (c, d) Using plasma. Interrupted growth is used to show that a longer second growth step results in longer second step growth at the base or root.<sup>68)</sup>

It should be noted that for interconnect applications, the relevant value is the wall density, not tube density. The wall density is larger than the tube density for multiwalled nanotubes, as shown in Fig. 10.

There are other ways to increase nanotube density. First, it is necessary to ensure that the tubes grow by root growth.<sup>68–71)</sup> This can be checked by the interrupted growth method, as seen in Fig. 15.<sup>68,72)</sup> One way to enforce root growth is a plasma treatment step that increases the interfacial bonding of the catalyst to the support layer. It





**Fig. 16.** (Color online) Raman spectra of high density forests at various excitation wavelengths.

also results in a narrower catalyst size distribution<sup>73)</sup> and often a narrower average diameter. H<sub>2</sub>, O<sub>2</sub>, and N<sub>2</sub> plasmas are effective<sup>20,68–71)</sup> and process gas optimisation.<sup>37,74,75)</sup> A further way is to minimise sintering of the catalyst for example by a short carburisation step.<sup>20)</sup>

#### 4. Discussion

The vertically aligned forests have been characterised by Raman spectroscopy, in order to estimate the ratio of semiconducting to metallic nanotubes. The radial breathing mode (RBM) spectra are shown in Fig. 16. The modes are assigned using the usual relationship,<sup>27)</sup>  $\omega = C_1/D + C_2$  with  $C_1 = 220.4 \pm 0.7$  and  $C_2 = 7.4 \pm 0.7$ . It is found that the chiralities have a random distribution. The chirality distribution is found to be random in terms of metallic to semiconducting ratio. It is true that some catalyst selection and/or treatment step can be used to enhance the metallic fraction.<sup>76)</sup> However, there are higher priorities that this for growth for vias, such as density and low temperature growth. Whereas in a field effect transistor (FET), metallic nanotubes must be removed as they prevent the FET from being switched off, for interconnects semiconducting nanotubes are just dead space but otherwise they are not deleterious.

#### 5. Process Integration

The nanotube growth process must then be made compatible with complementary metal oxide semiconductor (CMOS) fabrication processing. We are making considerable efforts to grow nanotubes on conducting substrates. There are two problems. First, metallic substrate have higher surfaces energies and thus inhibit the catalyst de-wetting process that forms the nano-particles. The second is that metals are reactive to oxygen and carbon, and can be converted into an insulating form. These processes are being studied by *in-situ* XPS and X-ray diffraction (XRD) to understand the preferred metal support.

A CMOS compatible growth process has been developed by Awano *et al.*<sup>19)</sup> and Dijon *et al.*<sup>29)</sup> In the Dijon process,

the bottom contact is made of Al–Cu alloy, with 99% Al, a previous form of interconnect material. The process steps are as follows. The surface is first given a cleaning step to remove oxides and other potentially insulating layers. The catalyst is deposited and annealed to make the nano-particles. Dijon *et al.*<sup>29,70)</sup> enforced root growth needed to ensure for high density mats by using an oxygen plasma treatment of the catalyst to increase the interfacial adhesion. Growth was carried out in a large area, low pressure CVD system. Growth continued until the nanotubes had emerged from the via holes. The holes around the nanotubes were then back-filled by Al<sub>2</sub>O<sub>3</sub> by atomic layer deposition to provide mechanical integrity. The samples were then planarised by chemical mechanical polishing (CMP).<sup>29)</sup> Then a top contact of Ti was applied, followed by a Pt overlayer. This is a similar set of steps as used by the Mirai group.<sup>18,29)</sup>

It is still necessary to ensure that the process temperature is sufficiently low, 400 °C, which might be achieved by deposition in a remote plasma<sup>19,38)</sup> as well as direct CVD.<sup>40)</sup> It is useful to study the chemical state of the various layers by *in-situ* XPS during growth to ensure that the reactive metals do not accidentally form insulating layers.<sup>69–71,77,78)</sup>

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